	Document ID	Issue Date	Pages	Title	Current OR
1	US 20040130632 A1	20040708	12	Imaging apparatus	348/223.1
2	US 20040101057 A1	20040527	9	Digital broadcast receiver and digital broadcast receiving method	375/240.25
3	US 20030071908 A1	20030417	65	Image pickup device, automatic focusing method, automatic exposure method, electronic flash control method and computer program	348/345
4	US 20020106036 A1	20020808	12	Bs digital broadcasting receiving device and bs digital broadcasting reciving method	375/329
5	US 20010047367 A1	20011129	30	Database transition system and program storage medium	707/200
6	US 6892197 B2	20050510	30	Database transition system and program storage medium	707/4
7	US 6813321 B1	20041102	11	Digital demodulator	375/329
8	US 6809724 B1	20041026	103	Display apparatus and portable information processing apparatus	345/169
9	US 6748037 B1	20040608	21	Digital broadcasting receiver	375/344
10	US 6717993 B1	20040406	45	Receiver	375/329
11	US 6700940 B1	20040302	14	Carrier reproduction circuit	375/326
12	US 6697440 B1	20040224	21	Demodulator of receiver	375/329
13	US 6690745 B1	20040210	22	Circuit for detecting the phase of received signal	375/316
14	US 6683921 B1	20040127	47	Received-signal absolute phasing apparatus of receiver	375/331

	Current XRef	Inventor
1		Shiraishi, Kenji
2	375/240.11	Shiraishi, Kenichi et al.
3	348/350	Sannoh, Masato et al.
4		Shiraishi, Kenichi et al.
5	707/103Y	Eda, Shinichirou et al.
6	707/10; 707/103R	Eda; Shinichirou et al.
7	375/326; 375/340; 375/376	Katoh; Hisakazu et al.
8	345/157; 715/788; 715/792; 715/798	Shiraishi; Atsushi et al.
9		Katoh; Hisakazu et al.
10		Shiraishi; Kenichi et al.
11	329/345; 329/346; 375/260; 375/329; 375/371; 375/375	Katoh; Hisakazu et al.
12	329/304; 375/279; 375/326	Shiraishi; Kenichi et al.
13	375/331; 375/365	Horii; Akihiro et al.
14	329/304; 375/284; 375/332; 375/371	Shiraishi; Kenichi et al.

	Document ID	Issue Date	Pages	Title	Current OR
15	US 6678342 B1	20040113	30	Absolute-phasing synchronization capturing circuit	375/362
16	US 6678336 B1	20040113	16	Hierarchical transmission digital demodulator	375/316
17	US 6661438 B1	20031209	104	Display apparatus and portable information processing apparatus	715/835
18	US 6639951 B1	20031028	11	Digital demodulator	375/326
19	US 6625239 B1	20030923	28	Circuit for capturing frame sync signal in receiver	375/354
20	US 6621508 B1	20030916	101	Information processing system	715/810
21	US 6597378 B1	20030722	100	Display device, portable information processing apparatus, information storage medium, and electronic apparatus	715/764
22	US 6526107 B1	20030225	21	Synchronization acquiring circuit	375/368
23	US 6097877 A	20000801	28	Digital recording and reproducing apparatus which multiplexes and records HDTV, SDTV and trick play data together on a magnetic tape	386/68
24	US 5867691 A	19990202	20	Synchronizing system between function blocks arranged in hierarchical structures and large scale integrated circuit using the same	713/400
25	US 5685006 A	19971104	18	Application specific integrated circuit having hierarchical structure and method of organizing such circuit using inheritance information	712/34
26	US 5448690 A	19950905	37	Image processing system enabling real-time output of image signal based on polygon image information	715/853

	Current XRef	Inventor
15	375/354; 375/371	Horii; Akihiro et al.
16	375/324	Katoh; Hisakazu et al.
17	715/781; 715/810; 715/837	Shiraishi; Atsushi et al.
18	375/329	Katoh; Hisakazu et al.
19	329/304; 370/206; 375/343; 375/355; 375/368	Shiraishi; Kenichi et al.
20	715/764; 715/778; 715/781; 715/835; 715/841	Shiraishi; Atsushi et al.
21	715/733; 715/751; 715/778; 715/810; 715/835	Shiraishi; Atsushi et al.
22	370/203; 370/206; 375/261	Katoh; Hisakazu et al.
23	386/111; 386/81	Katayama; Hironobu et al.
24		Shiraishi; Hajime
25	711/114; 711/122; 712/43; 716/17	Shiraishi; Hajime
26	345/443	Shiraishi; Naoto et al.

	Document ID	Issue Date	Pages	Title	Current OR
27	US 5159689 A	19921027	51	Processing apparatus with functional hierarchical structure including selective operation of lower level units by higher level units	712/208
28	US 5111388 A	19920505	48	Processing apparatus with functional hierarchical structure using corresponding hierarchical machine instruction fields	712/208
29	US 4947447 A	19900807	35	Method for data coding	382/246
30	US 4901225 A	19900213	50	Processing apparatus with hierarchical structure for implementing a machine instruction	712/212
31	JP 2003270356 A	20030925	9	VISIBILITY ESTIMATION DEVICE	
32	JP 2002125162 A	20020426	7	DIGITAL BROADCAST RECEIVER AND DIGITAL BROADCAST RECEIVING METHOD	
33	JP 2002007780 A	20020111	16	METHOD FOR ELECTRONIC COMMERCE	
34	JP 2000165463 A	20000616	8	DEMODULATOR	
35	JP 11220450 A	19990810	19	RECEPTION ERROR MEASUREMENT SYSTEM	
36	JP 11163957 A	19990618	11	HIERARCHICAL TRANSMISSION DIGITAL DEMODULATOR	
37	JP 10215291 A	19980811	12	BROADCAST RECEIVER	
38	JP 09016527 A	19970117	24	METHOD AND SYSTEM FOR LARGE SCALE DISTRIBUTED INFORMATION PROCESSING	
39	JP 07311772 A	19951128	6	DATA PRESERVATION DEVICE AND HIERARCHICAL DATA PRESERVATION SYSTEM	
40	JP 61156458 A	19860716	11	ARITHMETIC PROCESSING UNIT	
41	EP 1054537 A1	20001122	19	HIERARCHICAL TRANSMISSION DIGITAL DEMODULATOR	
42	WO 9929075 A1	19990610	34	HIERARCHICAL TRANSMISSION DIGITAL DEMODULATOR	

	Current XRef	Inventor
27	712/212	Shiraishi; Hajime
28		Shiraishi; Hajime
29	382/240; 382/251	Miyaoka; Shinichiro et al.
30	712/247	Shiraishi; Hajime
31		SHIRAISHI, SUSUMU
32		SHIRAISHI, KENICHI et al.
33		SHIRAISHI, KEIICHI
34		HORII, AKIHIRO et al.
35		TAKAGI, RYOSUKE et al.
36		SHIRAISHI, KENICHI et al.
37		HORII, AKIHIRO et al.
38		SUDA, SHINICHIRO et al.
39		SHIRAISHI, TOSHIHITO
40		SHIRAISHI, HAJIME
41		KATOH, HISAKAZU et al.
42		KATOH, HISAKAZU et al.

	Document ID	Issue Date	Pages	Title	Current OR
43	EP 560385 A2	19930915	22	Inter-hierarchy synchronizing system and an LSI using the same system.	
44	WO 9929075 A	19990610	16	Hierarchical transmission digital demodulator	
45	US 5448690 A	19950905	37	Image processing system for creating three=dimensional computer graphics in work station - has screen memory for storing polygon numbers stored in accordance with priority of polygons and polygon extracting device for dividing screen image into n-level hierarchy	
46	JP 07200654 A	19950804	18	Application specific integrated circuit with hierarchical structure - has programmable wiring mechanisms determining number of repeat times at functional block portion circuits and wirings between inheritance circuits, functional blocks, and data transfer switches	
47	EP 560385 A	19940706	20	Inter hierarchy synchronising system in LSI semiconductor device - has rendezvous units linking parent and child blocks in hierarchical structure to synchronise operation to local timing system	
48	EP 158320 A	19851016	50	Data processing apparatus with hierarchical functional structure - has operation code and operation object fields and functional blocks forming structure corresp. to hierarchy machine instructions	

	Current XRef	Inventor
43	340/FOR. 214	SHIRAISHI, HAJIME
44		HASHIMOTO, A et al.
45		FUJII, T et al.
46		SHIRAISHI, H
47		SHIRAISHI, H
48		SHIRAISHI, H